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<input type="checkbox"/>	L11	(710/100, 107, 110, 305-306).ccls.	3942
		<i>DB=PGPB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L10	(debug\$6 with (keyboard with controller\$2))	3
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<input type="checkbox"/>	L9	L8 and @pd > 20040729	0
<input type="checkbox"/>	L8	L6 and (keyboard with controller\$2)	9
<input type="checkbox"/>	L7	L6 and (keyborad with controller\$2)	0
<input type="checkbox"/>	L6	L5 and L3	62
<input type="checkbox"/>	L5	(debug\$6 with controller\$2)	1102
<input type="checkbox"/>	L4	L2 and L3	3
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## 1 High-speed bus debug and validation test challenges

*Hosman, M.;*

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[\[Abstract\]](#)[\[PDF Full-Text \(615 KB\)\]](#)

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## 2 A multigigahertz clocking scheme for the Pentium(R) 4 microproce

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*Malkin, R.A.;*

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*Dervisoglu, B.I.;*

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*Melear, C.;*

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*Filho, M.S.; Schneebeli, H.A.; Machado, A.C.;*

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*Wang Baoli; Li Changli; Guan Dinghua;*

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*Sakamoto, T.; Yoshida, T.; Yoshioka, S.; Fujimoto, Y.; Kobayashi, T.; Hiroi, T.*  
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